



Introduction to VLSI Circuits and Systems

John P. Uyemura

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Introduction to VLSI Circuits and Systems John P. Uyemura

Presents modern CMOS logic circuits, fabrication, and layout in a cohesive manner that links the material together with the system-level considerations.

* Chapter on Verilog HDL allows for rapid start-up.

* Illustrates the top-down design procedure used in modern VLSI chip design with an emphasis on variations in the HDL, logic, circuits and layout.

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