Digital Electronics Laboratory Experiments Using the Xilinx XC95108 CPLD with Xilinx Foundation: Design and Simulation Software (2nd Edition)

James Stewart, Chao-Ying Wang

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 This manual offers an easy-to-read, easy-to-follow approach to digital fundamentals through the use of Complex Programmable Logic Devices (CPLDs). The use of advanced logic device technology prepares readers for using an industry-standard design environment. The first shorter section of the book contains a set of lab jobs using a single TTL chip: the 74LS00 quad 2-input NAND gate, allowing students to build a few simple circuits immediately. The second section contains a set of hands-on lab jobs with step-by-step instructions on using the Xilinx XC95108 CPLD. With its comprehensive appendices, this manual can prove useful to those who work with large-scale programmable devices such as CPLDs and FPGAs in the fields of electronics and engineering.

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